

File 16:Gale Group PROMT(R) 1990-2001/May 02
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 File 160:Gale Group PROMT(R) 1972-1989
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 File 148:Gale Group Trade & Industry DB 1976-2001/May 02
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 File 621:Gale Group New Prod.Annou.(R) 1985-2001/May 02
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 File 636:Gale Group Newsletter DB(TM) 1987-2001/May 02
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 File 88:Gale Group Business A.R.T.S. 1976-2001/May 03
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 File 47:Gale Group Magazine DB(TM) 1959-2001/May 02
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 File 275:Gale Group Computer DB(TM) 1983-2001/May 01
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Set	Items	Description
S1	30898	VOCODER OR VODER OR (VOICE? OR SPEECH) (3N) (SYNTHE? OR RESYN- NTH? OR CODE? ? OR CODING OR DECOD??? OR ENCOD???) OR (SOUND? OR VERBAL OR VOCAL? OR SING? OR WORD? ?) (3N) (SYNTHE? OR RESYN- THE?)
S2	410235	VECTOR? OR DSP OR DSPS OR SIGNAL? ?(1N)PROCESS???? OR MATR- IX? OR MATRICES OR ARRAY?(1W)PROCESS???
S3	517268	SCALAR? OR PROTOCOL?
S4	179554	MULTIPROCESS? OR (MULTI OR MULTIPLE OR MANY OR SEVERAL OR - PLURAL? OR NUMEROUS OR MORE(1W)ONE OR THREE) (4W) (PROCESS???? - OR MICROPROCESS????)
S5	443827	(CELL OR CELLULAR OR MOBILE OR PORTABLE OR WIRELESS OR HAN- DHELD OR HAND()HELD OR CORDLESS OR RADIO OR WITHOUT(2W) (CORD? ? OR WIRE OR WIRES)) (5W) (TELEPHONE? OR PHONE OR PHONES) OR RA- DIOPHONE? OR RADIOTELEPHONE?
S6	0	S1(S)S2(S)S3(S)S4(S)S5
S7	106	S5(S)S4(S) (S1 OR S2 OR S3)
S8	19	S7 NOT PY=(1998:2001 OR 1995:1997)
S9	7	S8 NOT PY=(1992:1994)
S10	5	S9 NOT PD=19910627:19911231
S11	3	RD (unique items)
S12	46	TEXAS() INSTRUMENT?(S) (S5(S)S4 OR PROTOCOL? (2N) PROCESS???)
S13	27	S12 NOT PY=(1995:1997 OR 1998:2001)
S14	16	S13 NOT PY=1992:1994
S15	16	S14 NOT PD=19910627:19911231
S16	12	RD (unique items)
?		

11/3,K/1 (Item 1 from file: 16)
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01160652 Supplier Number: 41321538 (USE FORMAT 7 FOR FULLTEXT)

DSP chip sets get the call

Electronic Engineering Times, p74

May 7, 1990

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 512

Emeryville, Calif. -- **Three** new digital **signal processing** (DSP) chip sets ease the task of designing analog **cellular telephones** with high integration and bundled software. The **DSP Group Inc.** shoehorned much of the circuitry and utility software needed to provide analog audio, wideband data and supervisory audio tone (SAT) processing into three...

11/3,K/2 (Item 1 from file: 160)
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02312272

NEW PMI DYNAMIC RANGE PROCESSOR IDEAL FOR COMPANDING AND AGC APPLICATIONS

News Release August 10, 1989 p. 1

Today Precision Monolithics Inc. introduced the SSM-2120 Dynamic Range Processor that combines **multiple signal processing** functions on a single integrated circuit for the first time. Applications include **cellular phones**, wireless and cordless equipment, data communications, and professional, consumer, and automotive audio equipment. Housed in a 22-pin skinny-DIP package, the SSM-2120 permits...

... access to its two voltage controlled amplifiers (VCA,s) and level detectors for an unprecedented degree of flexibility. Depending on the connection of external components, **numerous dynamic range processing** functions can be realized including companding, automatic gain control, compression, peak limiting, noise gating, noise reduction, and sibilance processing.

...

11/3,K/3 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
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05163638 SUPPLIER NUMBER: 10771153 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Relational takes on OLTP. (relational technology in on-line transaction processing) (includes related article on IBM's research efforts in transaction processing)

Moad, Jeff

Datamation, v37, n10, p36(4)

May 15, 1991

ISSN: 1062-8363 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 2297 LINE COUNT: 00189

... machines to keep up with growing transactionprocessing demands on relational systems. One of these users is John Black, a systems architect with us West New **Vector Group Inc.**, the \$250 million Bell operating company unit based in Bellevue, Wash., that sells **cellular telephone** services in 14 western states and overseas. Several years ago, the company implemented its transaction-oriented order-entry/customer-maintenance application under IBM'S IMS hierarchical database. But, with business growing at about 7017(a year, New **Vector** has had to make frequent changes to its system. Since the inflexible IMS makes that difficult, New **Vector** wants to move to Oracle running on **multiprocessor** Sequent machines. But, says Black, even the largest symmetric **multiprocessor**

Sequent machine available would be overwhelmed by transaction applications New **Vector** is running now-and others that are in the works. "We need to be able to apply more than one system to a single database serving lots of users," says Black. Something like Parallel Server will allow networks of **multiple processors** to support growing numbers of users with no degradation." New **Vector** has endorsed Parallel Server and plans to install it 60 days after it becomes available on the Sequent loosely coupled system.

Black says New Vector..

16/3,K/1 (Item 1 from file: 16)
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01680768 Supplier Number: 42089172 (USE FORMAT 7 FOR FULLTEXT)
Tek debuts PEX terminal
Electronic Engineering Times, p64
May 20, 1991
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 307

... a trio of microprocessors to work. The XP29P terminal, compatible with the current PEX5R1 release from MIT, uses a 68030 from Motorola Inc. for network **protocol processing** and a 34020 graphics processor and 34082 floating-point unit from **Texas Instruments** Inc. to handle graphics and imaging.

Traditionally, FPUs have not been used in X terminals if they contain a dedicated graphics processor, but floating-point...

16/3,K/2 (Item 2 from file: 16)
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01635256 Supplier Number: 42021124 (USE FORMAT 7 FOR FULLTEXT)
X Window design: no pretty picture
Electronic Engineering Times, p43
April 22, 1991
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 2784

... be gained by a multiprocessing approach. You tend to give up in interprocessor communications overhead what you gain from off-loading.

Northwest already had a **Texas Instruments** Inc. 340-series CPU running the server code, so Northwest engineers wrote their own Ethernet code to go on that. They claim the result is superior to what they would have achieved with a separate **protocol processor**.

HP has taken a similar approach in its new terminal, though it employs a device of considerably greater throw weight: the 80960CA. "We just have ...

16/3,K/3 (Item 1 from file: 160)
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02124932
NEW SINGLE-CHIP TOKEN-RING COMPROCESSOR DELIVERS 16 MBPS PERFORMANCE, MANTAINS IBM (R) COMPATIBILITY
News Release December 6, 1988 p. 1

Texas Instruments today announced a single-chip 16 megabit-per-second (Mbps) token-ring communications network processor (COMMprocessor). Like its predecessor, the existing TMS380 Token-Ring chip...

... five chips. It integrates the functions performed by three of the five members of the first-generation TMS380 token-ring chip set: the TMS38010 Communications **Processor**, the TMS38010 **Protocol** Handler, and the TMS38030 System Interface. Additionally, the functions of two support circuits used with the first-generation chip set, both VLSI standard-cell (ASIC...

16/3,K/4 (Item 2 from file: 160)
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02079313

TI readies 16M token-ring chipset for market debut
Network World December 5, 1988 p. 17,18

Texas Instruments (TI) (Dallas, TX) has introduced a 16-Mbps token-ring chip set for 16-Mbit token-ring products. Some 16 local network vendors like 3Com...

... only commercial 16-Mbps chipset producer. The chipset includes a ring-interface chip, which is the token-ring wiring media interface and a communications network **processor** chip for **protocol processing**, transfer and buffer operations, according to L Adams, marketing mgr, open systems. Users will not be able to shift between 4-Mbps and 16-Mbps...

16/3,K/5 (Item 3 from file: 160)
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01417086

TI Pares Chip Set For IBM Token Ring.
ELECTRONICS October 2, 1986 p. 33,36

Texas Instruments is developing a 2-chip set for IBM's token-ring LAN. The new chip set, code-named Eagle, will replace 5 members of TI...

... 5 current-generation TMS380 token-ring adapter chips onto a single 132-pin device. The new chip will incorporate the functions of the 38010 communications **processor**, the 38020 **protocol** handler, and the 38030 system interface chip. For the second chip, TI is combining the functions of its 2 bipolar ring-interface devices onto a...

16/3,K/6 (Item 4 from file: 160)
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01266597

New Token-Ring ICs Brighten TI's prospects.
ELECTRONIC ENGINEERING TIMES October 21, 1985 p. 1,14

Texas Instruments has introduced its 5-chip, token-ring set of ICs. The token-ring evaluation set includes 3 complete sets of LAN ICs, a set of ...

... TMS38030 System Interface stands between the host computer's internal bus and the token-ring LAN adapter bus. Communications are supported by the TMS38010 Communications **Processor** and TMS38020 **Protocol** Handler. The Protocol Handler, in turn, deals with 2 ring-interface chips. The TMS38051 and TMS38052 chips combine the low-power Schottky and bipolar logic...

16/3,K/7 (Item 5 from file: 160)
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01266522

TI unveils adapter chip set for IBM LAN.
ELECTRONIC NEWS October 21, 1985 p. 38

Texas Instruments (Dallas, Texas) has introduced an adapter chip set for IBM's new token-ring LAN. The TMS380 set, which meets the ANSI/IEEE 802.5...

... shielded twisted pair and fiber optics, and can be upgraded to 16 Mbps. Components of the chip set include the TMS38030 system interface, TMS38010

*communications processor, TMS38020 protocol handler, TMS39051 ring interface transceiver, and TMS38052 ring interface controller.
...

16/3,K/8 (Item 1 from file: 148)
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04139882 SUPPLIER NUMBER: 07895356 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Simpact adapter links MicroVAXes directly to Token-Ring nets. (Simpact Associates Inc. TCI 2000 Token Ring board/software series) (product announcement)
Musich, Paula
PC Week, v6, n46, p88(1)
Nov 20, 1989
DOCUMENT TYPE: product announcement ISSN: 0740-1604 LANGUAGE:
ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 351 LINE COUNT: 00028

... Simpact's system, the network adapter, is a board for the MicroVAX's Q-Bus that plugs into the backplane of the MicroVAX. Based on Texas Instruments Inc.'s Token-Ring chip set, it includes a Motorola Inc. 68020 microprocessor and 2M bytes of memory for protocol processing.

NetBIOS, TCP/IP and IEEE 802.2 interfaces are available now, and an interface that supports LU 6.2 protocols is slated to be available...

16/3,K/9 (Item 1 from file: 47)
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03154668 SUPPLIER NUMBER: 06886709 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Connectivity Newswire
PC Week, v5, n32, pC7(1)
Aug 8, 1988
ISSN: 0740-1604 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 1133 LINE COUNT: 00090

... more than the basic ACS 4030 bridge. The company is located at 720 Santa Barbara St., Santa Barbara, Calif. 93101 (805) 963-9431.

LAN Hardware: Texas Instruments is now shipping versions of its Token-Ring chips in plastic packages. The replacement of ceramic packaging with plastic on the company's TMS380 Token-Ring communications processor and protocol handler reduces the cost of those chips by 10 percent to 25 percent, company officials said. The chips are available through any TI sales office or authorized distributor for between \$17 and \$25 per chip, based on purchase of 10,000 units. Texas Instrument 's Semiconductor Group can be reached at P.O. Box 809066, Dallas Texas 75380-9066 (800) 232-3200.

Printer Emulation: Dataproducts Corp. recently announced two...

16/3,K/10 (Item 2 from file: 47)
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03085495 SUPPLIER NUMBER: 06654829 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Two integrated circuits help developers design Token-Ring products. (Connectivity section) (product announcement)
Garretson, Rob
PC Week, v5, n17, pC27(1)
April 26, 1988
DOCUMENT TYPE: product announcement ISSN: 0740-1604 LANGUAGE:
ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 424 LINE COUNT: 00033

ABSTRACT: **Texas Instruments** introduces two application-specific integrated circuits (ASICs) for IBM Token-Ring-compatible product development. The ASICs were co-developed by IBM and perform most of...

...Unit replaces 14 components in a typical Token-Ring design and provides the interface between memory on a network card and the TMS380 chip, which **processes** Token-Ring **protocols**. The under-\$9 PC Bus Interface Unit replaces 22 parts and provides the interface between the TMS380 and a PC's I-O bus.

16/3,K/11 (Item 1 from file: 275)
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01297473 SUPPLIER NUMBER: 07282657 (USE FORMAT 7 OR 9 FOR FULL TEXT)

As chips appear, the LANscape will alter.

Vaughan, Jack

ESD: The Electronic System Design Magazine, v19, n1, p24(1)

Jan, 1989

ISSN: 0893-2565 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 703 LINE COUNT: 00056

ABSTRACT: New design generations of VLSI local area network (LAN) chips are expected to cause system integrators to rethink their LAN strategies. **Texas Instruments** ' new 20 MHz TMS380C16 chip set integrates communications **processing**, **protocol** handling, and system interfacing; it supports both 4- and 16-Mbit per sec operation, and may allow IBM Token Ring net to leapfrog Ethernet. National...

16/3,K/12 (Item 2 from file: 275)
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01174274 SUPPLIER NUMBER: 04366506

Chip set paves the way to Token-Ring access.

Carlo, James T.; Samsen, Gerald R.

Mini-Micro Systems, v19, n11, p121(4)

Sept, 1986

ISSN: 0364-9342 LANGUAGE: ENGLISH RECORD TYPE: ABSTRACT

...ABSTRACT: products for the network, and IBM's incorporation of the Physical and Data Link layers of the OSI model of the ISO guarantees compatible connections. **Texas Instruments** Inc. joined with IBM to develop a chip set to provide connections between the OSI model and the Token-Ring Network, the TMS380, which provides...

...functions of a local area network adapter are integrated into a five-chip set in the TMS380 architecture: the TMS38030 system interface, the TMS38010 communications **processor**, the TMS38020 **protocol** handler, and the TMS38051 and TMS38052 ring-interface pair. The design and operation of the TMS380 chip set are described, and different ways the chip...